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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Douglas Roger Pulley, et al. (as amended)

Serial No.: 09/617,587

Filed: July 18, 2000

For: "RECEIVER CIRCUIT"

Our Ref: B-3970 618055-7

Date: November 20, 2000

CLAIM TO PRIORITY UNDER 35 U.S.C. 119

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Sir:

[X] Applicants hereby make a right of priority claim under 35 U.S.C. 119 for the benefit of the filing date(s) of the following corresponding foreign application(s):

COUNTRY	FILING DATE	SERIAL NUMBER
United Kingdom	19 July 1999	9916894.0

[ ] A certified copy of each of the above-noted patent applications was filed with the Parent Application No.________________.

[X] To support applicant's claim, a certified copy of the above-identified foreign patent application is enclosed herewith.

[ ] The priority document will be forwarded to the Patent Office when required or prior to issuance.

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first-class mail in an envelope addressed to the "Commissioner of Patents and Trademarks, Washington, D.C. 20231", on November 20, 2000 by Karyn Lao

Respectfully submitted,

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I, the undersigned, being an officer duly authorised in accordance with Section 74(1) and (4) of the Deregulation and Contracting Out Act 1994, to sign and issue certificates on behalf of the Comptroller-General, hereby certify that annexed hereto is a true copy of the documents as originally filed in connection with the patent application identified therein together with the Statement of inventorship and of right to grant of a Patent (Form 7/77), which was subsequently filed.

I also certify that the attached copy of the request for grant of a Patent (Form 1/77) bears a correction, effected by this office, following a request by the applicant and agreed to by the Comptroller-General.

I also certify that by virtue of an assignment registered under the Patents Act 1977, the application is now proceeding in the name as substituted.

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[illegible] Evans.

Da[illegible] October 2000.

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Statement of inventorship and of right to grant of a patent

1.	Your reference	HL71431/000/DCO
2.	Patent application number *(if you know it)*	9916894.0
3.	Full name of the or of each applicant	CONEXANT SYSTEMS, INC.
4.	Title of the invention RECEIVER CIRCUIT	
5.	State how the applicant*(s)* derived the right from the inventor*(s)* to be granted a patent By virtue of the employment of the inventor by Oak Technology Limited; an agreement between Oak Technology Limited and Oak Technology Inc.; and an assignment from Oak Technology, Inc. to Conexant Systems, Inc.	
6.	How many, if any, additional Patent Forms 7/77 are attached to this form? *(See note (c))*	NONE
7.		I/We believe that the person(s) named over the page *(and on any extra copies of this form)* is/are the inventor(s) of the invention which the above patent application relates to. Signature [signature] Date 13 September 2000
8.	Name and daytime telephone number of person to contact in the United Kingdom	Mr. D. C. O'Connell [0117] 9103200

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6389-65

Enter the full names, addresses and postcodes of the inventors in the boxes and underline the surnames

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Patents ADP number *(if you know it)*

Reminder:
Have you signed this form?

GB9916894.0

By virtue of a direction given under Section of the Patents Act 1977, the application is proceeding in the name of

CONEXANT SYSTEMS, INC.,
4311 Jamboree Road
Newport Beach
California 92660-3095
United States of America

[ADP No. 68443704001]

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1. Your reference	HL71431/000/DCO
2. Patent application number *(The Patent Office will fill in this part)*	**9916894.0** 19 JUL 1999
3. Full name, address and postcode of the or of each applicant *(underline all surnames)*	Oak Technology, Inc. 139 Kifer Court Sunnyvale California 94086 USA SECTION 30 (1977 ACT) APPLICATION FILED 18/5/00.
Patents ADP number *(if you know it)* If the applicant is a corporate body, give the country/state of its incorporation	7643125001 USA — DELAWARE A/L 17/8/99 HRB
4. Title of the invention Receiver Circuit	
5. Full name of your agent *(if you have one)*	Haseltine Lake & Co.
"Address for service" in the United Kingdom to which all correspondence should be sent *(including the postcode)*	Imperial House 15–19 Kingsway London WC2B 6UD
Patents ADP number *(if you know it)*	34001
6. If you are declaring priority from one or more earlier patent applications, give the country and the date of filing of the or of each of these earlier applications and *(if you know it)* the or each application number	Country / Priority application number *(if you know it)* / Date of filing *(day/month/year)*
7. If this application is divided or otherwise derived from an earlier UK application, give the number and the filing date of the earlier application	Number of earlier application / Date of filing *(day/month/year)*
8. Is a statement of inventorship and of right to a grant of patent required in support of this request? *(Answer "Yes" if:* *a) any applicant named in part 3 is not an inventor, or* *b) there is an inventor who is not named as an applicant, or* *c) any named applicant is a corporate body.* *See note (d))*	Yes

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Claim*(s)*	4
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Statement of inventorship and right to a grant of patent *(Patents Form 7/77*	0
Request for preliminary examination and search *Patents Form 9/77)*	1
Request for substantive examination *(Patents Form 10/77)*	0
Any other documents *(please specify*	-

11.

I/We request the grant of a patent on the basis of this application

Signature Hasseltine Lake & Co

Date 15 July 1999

12. Name and daytime telephone number of person to contact in the United Kingdom

D.C. O'Connell [0117] 9103200

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RECEIVER CIRCUIT

TECHNICAL FIELD OF THE INVENTION

This invention relates to a receiver circuit, in particular for receiving signals in which a portion of a transmitted signal is repeated after a known time interval.

BACKGROUND OF THE INVENTION

The European DVB-T (Digital Video Broadcasting - Terrestrial) standard for digital terrestrial television (DTT) uses Coded Orthogonal Frequency Division Multiplexing (COFDM) of transmitted signals, which are therefore transmitted as OFDM symbols.

Received signals are sampled in a receiver, and accurate reception and demodulation of signals therefore requires accurate knowledge of the positions of the beginning and end of each OFDM symbol.

In particular, DVB-T COFDM signals include a cyclic prefix to each active symbol, which is repeated after a known and fixed time interval. These cyclic prefixes must be correctly removed before demodulation, or the demodulation performance can be seriously degraded.

The fact that the prefix in the COFDM signals is repeated can be used initially to find the position of the prefix, by calculating a running correlation between received portions which are received separated by the known time interval. A very high correlation will indicate the presence of a repeated portion. However, this does not allow correction for any changes in position caused by subsequent variations in sampling rate.

SUMMARY OF THE INVENTION

The present invention provides a receiver which overcomes some of the disadvantages of the prior art.

This invention relates in a first aspect to a receiver which can maintain the assumed position of the active symbols in the signal accurately, as compared with the actual position in the received signal, thereby advantageously allowing feedback control of the sample position of the receiver.

According to a second aspect of the invention, there is provided a method of processing received signals, and controlling the sampling position of a receiver.

In particular, according to the invention, there is provided a receiver circuit, comprising:

a sampler, for taking digital samples of a received signal, said received signal including at least a first portion and a second portion which repeats the content of the first portion after a repeat interval;

a processing device, for processing the digital samples on the basis of an assumed position of the first and second portions in the received signal;

at least one correlator for measuring:

a first correlation between a first group of samples including at least samples around the beginning of the first portion of the signal, and a second group of samples including at least samples around the beginning of the second portion of the signal; and

a second correlation between a third group of samples including at least samples around the end of the first portion of the signal, and a fourth group of

samples including at least samples around the end of the second portion of the signal;

means for comparing the measured first and second correlations to produce a comparison output; and

means for determining the assumed position of the first and second portions on the basis of the comparison output in order to tend to equalize the first and second correlations.

Preferably, the first, second, third and fourth groups of samples include samples immediately preceding and immediately following the respective beginning or end point of the first or second portion.

BRIEF DESCRIPTION OF DRAWINGS

Figure 1 is a schematic illustration of a part of a receiver circuit in accordance with the invention.

Figure 2 is an explanatory diagram provided for a better understanding of the present invention.

DETAILED DESCRIPTION OF PREFERRED EMBODIMENTS

Figure 1 shows a section of a receiver circuit relevant to the present invention. Typically, in the exemplary case of a digital terrestrial television signal receiver, for example receiving signals using the DVB-T standard with Coded Orthogonal Frequency Division Multiplexing, the receiver will include an antenna (not shown), and a tuner (not shown) for receiving the signals and downconverting to an intermediate frequency.

The receiver further includes a sampler 10 which receives signals, after conversion to baseband, at an input 12. For example, the sampler is preferably a

voltage controlled crystal oscillator with an analog-digital converter or a digital resampler, for producing baseband digital I and Q samples. In this example, the sampler produces (64/7)Msamples/second for both I and Q samples. The sampler is controllable in the sense that its sampling position can be adjusted. Output signals from the sampler 10 are supplied to processing devices (not shown) which, amongst other things, remove the cyclic components which precede each active symbol. In order to be able to do this accurately, the sampling position of the sampler 10 must be controlled such that the assumed position of the start of each symbol accurately coincides with the actual position in the received signal. Where the sampler 10 is a resampler, this control of the sampling position is achieved by adjusting its phase.

The received COFDM signal includes a portion which is repeated after a known and fixed time interval. Specifically, in this example it includes a portion which is 64 samples long, and which is repeated after an interval (the repeat interval) of 2048 samples (measured from the start of the portion to the start of the repeated portion).

It will be appreciated that the order in which signals are downconverted to baseband, converted to I and Q, and sampled, is not relevant to the invention.

It should also be noted that, while several parameters quoted herein relate specifically to the current United Kingdom specification for DVB-T, the values of such parameters are not relevant to the invention, which may be applied to any suitable signal

format.

The sampled signal output from the sampler 10 is supplied to a first delay element 14 and a second delay element 16, which applies a delay having a duration of two samples. The first delay element effectively advances the signal by a duration of two samples. Of course, this is not possible. In practice, the first delay element actually applies a delay of twice two samples, and there is no second delay element, with the extra two sample delay being compensated later in the processing. The exact duration of the delays is not critical, as it could be any number of samples, conveniently an integer number. A small delay gives improved noise performance, while a large delay increases the range of errors which can be corrected in each measurement and correction cycle.

The signal from the first delay element 14 is applied to a first correlation combiner 18, which includes a third delay element 20, which applies a delay equal to the repeat interval, that is, 2048 samples. A multiplier 22 receives as a first input the signal from the first delay element 14, and as a second input the delayed output from the third delay element 20.

The correlation between these two inputs is determined on a sample-by-sample basis in the multiplier 22, and output to a further block 24, which includes an integrator 26. The integrator 26 accumulates the results of the individual sample-by-sample correlations determined by the multiplier 22, and a sampling switch 28 gates the output and resets the integrator to provide an output correlation value, measured over the whole 64

samples of the repeated portion of the signal, to a first input of a subtractor 30. A running correlation is used initially to find the position of the repeated portion of the signal, so that the correlations described above are calculated only for the repeated portion of the signal.

Because the first delay element 14 effectively advanced the signal, this output is regarded as an early correlation.

Similarly, the output from the second delay element 16 is applied to a second correlation combiner 32, which includes a fourth delay element 34, which applies a delay equal to the repeat interval. Thus, with a repeat interval of 2048 samples, the fourth delay element 30 applies a delay of 2048 samples. A second multiplier 36 receives as a first input the signal from the second delay element 16, and as a second input the further delayed output from the fourth delay element 34.

The correlation between these two inputs is determined on a sample-by-sample basis in the multiplier 36, and output to an further block 38, which includes an integrator 40. The integrator 40 accumulates the results of the individual sample-by-sample correlations determined by the multiplier 36, and a sampling switch 42 gates the output and resets the integrator to provide an output correlation value, measured over the whole 64 samples of the repeated portion of the signal, to a second input of the subtractor 30.

Because the second delay element 16 delayed the signal, this output is regarded as a late correlation.

The correlation result for each OFDM symbol, R, is the magnitude of the complex correlation across N

samples of the cyclic repeat:

$$R = \left| \sum_{m=0}^{N-1} x_m x^{*}_{m+N_R} \right|$$

where * denotes the complex conjugate of a complex value, x_k are the samples of the signal and N_R is the number of samples between a sample of the cyclic prefix and its repeat. Either x_m or x_{m+N_R} maybe conjugated in this calculation and m=0 is taken to be the first sample of the assumed start of the cyclic prefix for a particular symbol.

The early correlation can be written as:

$$R_E = \left| \sum_{m=0}^{N-1} x_{m-2} x^{*}_{m-2+N_R} \right|$$

and the late correlation as:

$$R_L = \left| \sum_{m=0}^{N-1} x_{m+2} x^{*}_{m+2+N_R} \right|$$

The subtractor 30 receives the two correlation values as inputs, and therefore provides an output signal which is a measure of the difference between the correlation values calculated in the correlation combiners 18, 32 respectively. The full significance of this will be described in more detail with reference to Figure 2 below.

More specifically, the difference between the correlation values is taken to be proportional to the time error in the initially assumed sampling position. Thus:

$$\Delta t \propto \left|\sum_{m=0}^{N-1} x_{m-2} x^{*}_{m-2+N_R}\right| - \left|\sum_{m=0}^{N-1} x_{m+2} x^{*}_{m+2+N_R}\right|$$

The output signal from the subtractor 30 is supplied to a feedback loop filter 44 which appears in a feedback loop 46, and the output thereof is applied to the sampler 10 to control the sampling position.

Thus, if the result of the correlation calculations is that the input signal is found to be more closely correlated with the delayed signal or the effectively advanced signal, a correction is applied to the sampling position which will tend to equalize these correlations.

A more detailed explanation of the operation of the circuit will now be given with reference to Figure 2.

Figure 2 is a partial schematic illustration (not to scale) of the time history of a digitally sampled received COFDM signal. The signal includes a first portion 50, and a second portion 52, which is identical thereto and can therefore be seen as a repeat of the first portion. The signal also includes a third portion 54, and a fourth portion 56, which is identical thereto and can therefore be seen as a repeat of the third portion. The first, second, third and fourth portions 50, 52, 54, 56 each have a duration 58 of 64 samples.

The start of the second portion is 2048 samples after the start of the first portion, and the start of the fourth portion is 2048 samples after the start of the third portion. Thus, the repeat period is 2048 samples. Therefore if either the first or third portion of the signal were delayed by 2048 samples, it would be found to be exactly correlated (ignoring distortions,

noise, etc.) with the signal actually being received at that time.

When demodulating signals, it is important to know exactly when to expect to receive the start of each active symbol. This also allows other data, for example the cyclic prefixes which appear before each active symbol, to be removed. An error can mean that the receiver has a reduced ability to remove "ghost" images from the received signal, or can mean that the receiver is unable to reproduce any picture at all.

Figure 2 shows a delay 60 of 2048 samples as applied by the delay element 20 to a signal portion 62 which is two frames in advance of the portion 50 which is to be repeated, and which produces a delayed signal portion 64. Thus, the correlator 22 measures the correlation between the delayed signal portion 64 and the signal portion actually received at the same time. To the extent that signal portion 62 overlaps with signal portion 50, the delayed signal portion 64 is perfectly correlated (again ignoring distortions, noise, etc.) with the signal portion actually received at the same time. However, to the extent that signal portion 62 does not overlap with signal portion 50, the delayed signal portion 64 is broadly uncorrelated with the signal portion actually received at the same time.

Figure 2 also shows a delay 66 of 2048 samples as applied by the delay element 34 to a signal portion 68 which is two samples behind the portion 50 which is to be repeated, and which produces a delayed signal portion 70. Thus, the correlator 36 measures the correlation between the delayed signal portion 70 and the signal portion actually received at the same time. To the

extent that signal portion 68 overlaps with signal portion 50, the delayed signal portion 70 is perfectly correlated (again ignoring distortions, noise, etc.) with the signal portion actually received at the same time. However, to the extent that signal portion 68 does not overlap with signal portion 50, the delayed signal portion 70 is broadly uncorrelated with the signal portion actually received at the same time.

If the assumed sampling position is exactly synchronized with the transmitted signal, then the signal portion 62 would begin exactly two samples before the signal portion 50. The delayed signal portion 64 would then be correlated with the signal portion actually received at the same time for 62 samples out of 64, and uncorrelated for the remaining 2 samples out of 64. Similarly, the delayed signal portion 70 would then be correlated with the signal portion actually received at the same time for 62 samples out of 64, and uncorrelated for the remaining 2 samples out of 64.

Thus, taken over many OFDM symbols, the average values of the measures of correlation, as determined by the two correlation combiners 18, 32, would be exactly equal.

If, by contrast, the sampling position were slightly in advance of the received signal, the signal portion 62 would overlap with signal portion 50 for longer than before, and the delayed signal portion 64 would be more highly correlated with the signal portion actually received at the same time. At the same time, the signal portion 68 would overlap with signal portion 50 for a shorter time than before, and the delayed signal portion 70 would be less highly correlated with

the signal portion actually received at the same time.

Conversely, if the sampling position were slightly retarded relative to the received signal, the signal portion 62 would overlap with signal portion 50 for a shorter time than before, and the delayed signal portion 64 would be less highly correlated with the signal portion actually received at the same time. At the same time, the signal portion 68 would overlap with signal portion 50 for a longer time than before, and the delayed signal portion 70 would be more highly correlated with the signal portion actually received at the same time.

Returning to Figure 1, therefore, a zero output from the filter 44 is produced when the symbol start position of the receiver is exactly synchronized with the received signal, and produces no change in the sampling position. However, a non-zero output from the filter 44 is produced when the sampling position of the receiver is not exactly synchronized with the received signal, and is fed back to control the sampler 10 to produce a change in the sampling position. This change acts to bring the sampling position of the receiver into synchronization with the received signal.

The offset period of two samples, as described above, will often be greater than the actual offset. That being so, the last 60 samples of the signal portion 62 should be exactly correlated (again ignoring distortions, noise, etc.) with the last 60 samples of the signal portion 64, with any uncorrelation being confined to the first 4 samples. It is therefore sufficient to calculate the correlation only during these first 4 samples. Similarly, the first 60 samples

of the signal portion 68 should be exactly correlated (again ignoring distortions, noise, etc.) with the first 60 samples of the signal portion 70, with any uncorrelation being confined to the last 4 samples. It is therefore sufficient to calculate the correlation only during these last 4 samples.

In other words, we can assume that, on average, the difference between the overlapping portions of the two correlations is zero. Hence, it is possible to use the following approximation, if calculated over a sufficiently large number of symbols.

$$\Delta t \propto Average\left\{\left|\sum_{m=0}^{3} x_{m-2}\, x^{*}_{m-2+N_R}\right| - \left|\sum_{m=N-4}^{N-1} x_{m+2}\, x^{*}_{m+2+N_R}\right|\right\}$$

This modification therefore advantageously reduces the calculations and storage required.

The use of an offset period of two samples means that this is the largest error which can be corrected in each measurement and correction cycle. In the event that the actual offset is greater than two samples, then a correction of two samples is applied in each cycle, until the offset becomes less than two samples.

There are therefore disclosed a receiver circuit, and a method of controlling a sampling position therein, which allows exact synchronization to be achieved between the sampling position and the received sample position.

CLAIMS

1. A receiver circuit, comprising:

a sampler, for taking digital samples of a received signal, said received signal including at least a first portion and a second portion which repeats the content of the first portion after a repeat interval;

a processing device, for processing the digital samples on the basis of an assumed position of the first and second portions in the received signal;

at least one correlator for measuring:

a first correlation between a first group of samples including at least samples around the beginning of the first portion of the signal, and a second group of samples including at least samples around the beginning of the second portion of the signal; and

a second correlation between a third group of samples including at least samples around the end of the first portion of the signal, and a fourth group of samples including at least samples around the end of the second portion of the signal;

means for comparing the measured first and second correlations to produce a comparison output; and

means for determining the assumed position of the first and second portions on the basis of the comparison output in order to tend to equalize the first and second correlations.

2. A receiver circuit as claimed in claim 1, wherein the first, second, third and fourth group of samples each have the same length as the first and second portions of the signal.

3. A receiver circuit as claimed in claim 2, wherein the first group of samples is offset relative to the first portion of the signal, the second group of samples is offset relative to the second portion of the signal, the third group of samples is offset relative to the first portion of the signal, and the fourth group of samples is offset relative to the second portion of the signal, the durations of said offsets all being equal.

4. A receiver circuit as claimed in claim 3, wherein the durations of said offsets are all equal to two sample periods.

5. A receiver circuit as claimed in claim 1, wherein the first group of samples includes a predetermined number of samples at the beginning of the first portion of the signal, the second group of samples includes a predetermined number of samples at the beginning of the second portion of the signal, the third group of samples includes a predetermined number of samples at the end of the first portion of the signal, and the fourth group of samples includes a predetermined number of samples at the end of the second portion of the signal.

6. A method for receiving signals, the method comprising:

taking digital samples of a received signal, said received signal including at least a first portion and a second portion which repeats the content of the first portion after a repeat interval;

processing the digital samples on the basis of an assumed position of the first and second portions in the received signal;

measuring a first correlation between a first group of samples including at least samples at the beginning of the first portion of the signal, and a second group of samples including at least samples at the beginning of the second portion of the signal; and

measuring a second correlation between a third group of samples including at least samples at the end of the first portion of the signal, and a fourth group of samples including at least samples at the end of the second portion of the signal;

comparing the measured first and second correlations to produce a comparison output; and

determining the assumed position of the first and second portions on the basis of the comparison output in order to tend to equalize the first and second correlations.

7. A method as claimed in claim 6, wherein the first, second, third and fourth group of samples each have the same length as the first and second portions of the signal.

8. A method as claimed in claim 7, wherein the first group of samples is offset relative to the first portion of the signal, the second group of samples is offset relative to the second portion of the signal, the third group of samples is offset relative to the first portion of the signal, and the fourth group of samples is offset relative to the second portion of the signal, the durations of said offsets all being equal.

9. A method as claimed in claim 8, wherein the durations of said offsets are all equal to two sample periods.

10. A method as claimed in claim 6, wherein the first group of samples includes a predetermined number of samples at the beginning of the first portion of the signal, the second group of samples includes a predetermined number of samples at the beginning of the second portion of the signal, the third group of samples includes a predetermined number of samples at the end of the first portion of the signal, and the fourth group of samples includes a predetermined number of samples at the end of the second portion of the signal.

11. A receiver circuit, for processing a received signal, said received signal including at least a first portion and a second portion which repeats the content of the first portion after a repeat interval, the receiver comprising at least one correlator, for calculating an early correlation and a late correlation, the early correlation being measured between samples ahead of an assumed first portion start position and ahead of an assumed second portion start position, and the late correlation being measured between samples behind an assumed first portion end position and behind an assumed second portion end position, and revising the assumed start and end positions on the basis of a calculated difference between the early correlation and the late correlation.

ABSTRACT

RECEIVER CIRCUIT

A receiver circuit is for processing a received signal which includes at least a first portion and a second portion which repeats the content of the first portion after a repeat interval. For example, the receiver may be for DVB-T signals using COFDM. In order to ensure that the estimated symbol start position is accurate, the receiver calculates two correlation values, namely an early correlation and a late correlation. The early correlation is measured between samples ahead of an assumed first portion start position and ahead of an assumed second portion start position, and the late correlation is measured between samples behind an assumed first portion end position and behind an assumed second portion end position. When the assumed start and end positions are accurate, the early and late correlations are equal, and so the assumed start and end positions are controlled to equalize the early correlation and the late correlation.

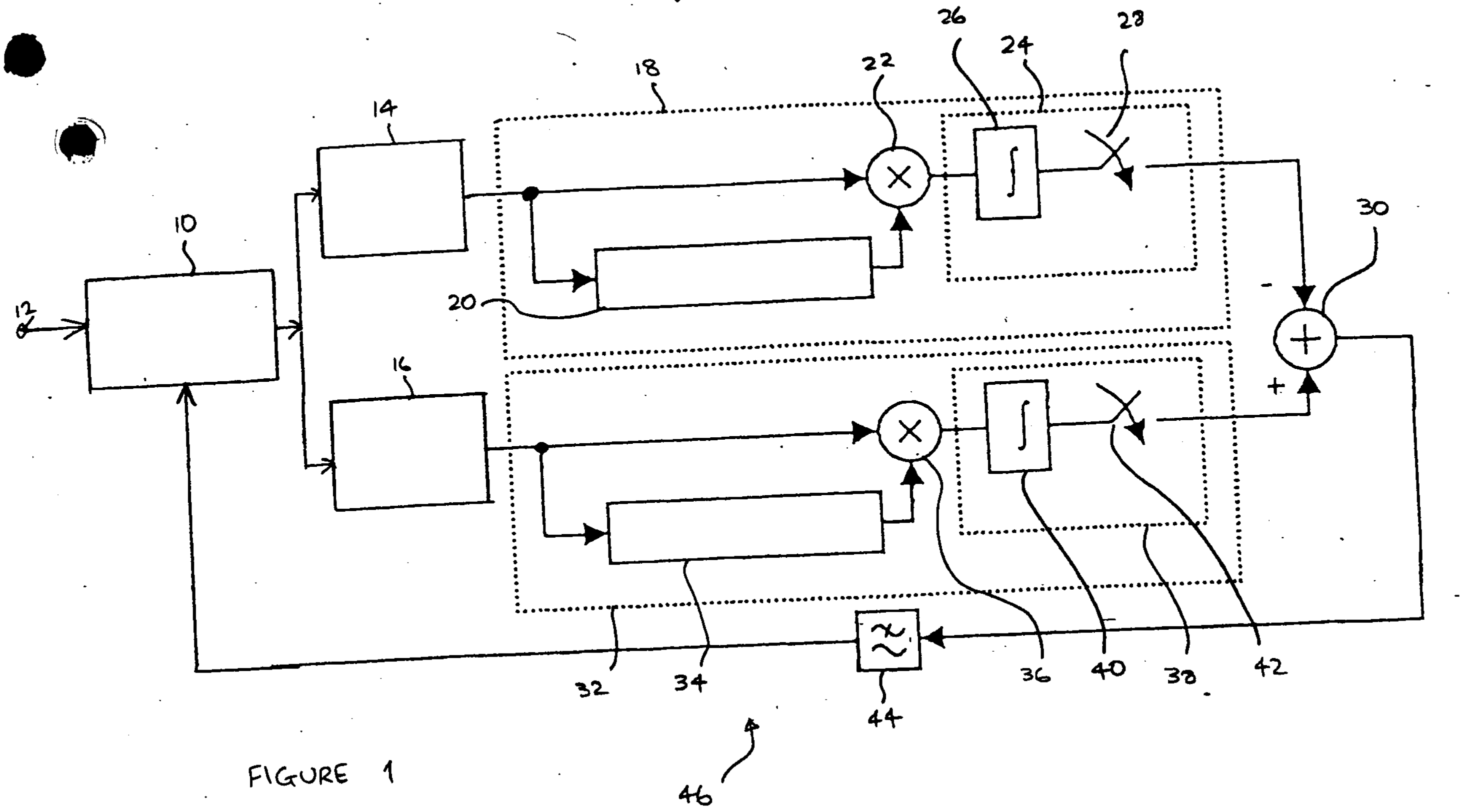

FIGURE 1

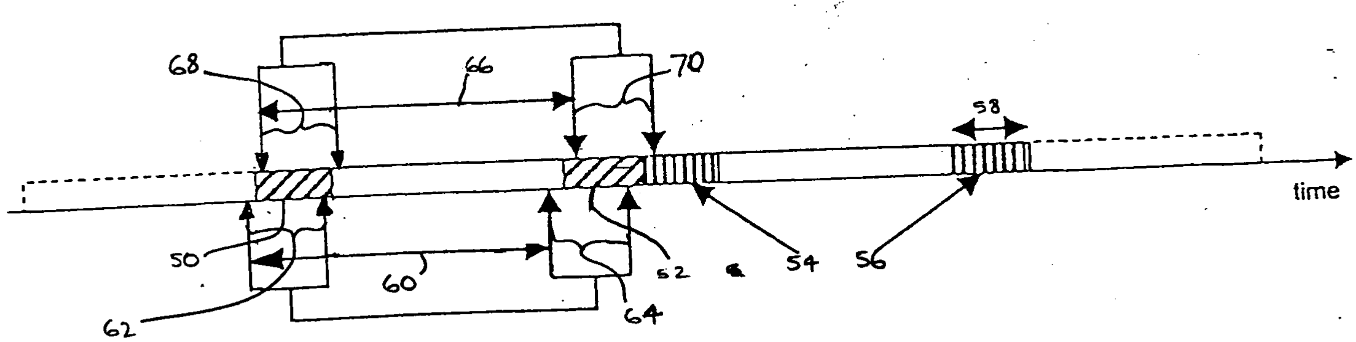

FIGURE 2